



AMENDMENT TRANSMITTAL LETTER				Docket No. ROH-026	
Application No. 09/665,663	Filing Date September 20, 2000	Examiner W.D. Coleman	Art Unit 2823		
Applicant(s): Shigeyuki UEDA					
Invention: SEMICONDUCTOR CHIP AND METHOD OF PRODUCING THE SAME					
TO THE COMMISSIONER FOR PATENTS					
Transmitted herewith is an amendment in the above-identified application.					
The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	10	- 20 =		x	0.00
Independent Claims	2	- 3 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify): Extension for response within third month					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					
<input checked="" type="checkbox"/> Large Entity <input type="checkbox"/> Small Entity					
<input type="checkbox"/> No additional fee is required for this amendment.					
<input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of \$ _____. A duplicate copy of this sheet is enclosed.					
<input type="checkbox"/> A check in the amount of \$ _____ to cover the filing fee is enclosed.					
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge and credit Deposit Account No. <u>18-0013</u> as described below. A duplicate copy of this sheet is enclosed.					
<input checked="" type="checkbox"/> Credit any overpayment.					
<input checked="" type="checkbox"/> Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.					
 Carl Schaukowitch Attorney Reg. No.: 29,211				Dated: <u>May 14, 2003</u>	
RADER, FISHMAN & GRAUER PLLC 1233 20th Street, N.W. Suite 501 Washington, DC 20036 (202) 955-3750 Customer No. 23353					

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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of:

Shigeyuki UEDA

Application No.: 09/665,663

Filed: September 20, 2000

For: SEMICONDUCTOR CHIP AND METHOD OF PRODUCING THE SAME

Attorney Docket No.: ROH-026

Examiner: W. D. Coleman

Art Unit: 2823

Confirmation No.: 5545

AMENDMENT under 37 CFR 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In response to the non-final Office Action dated November 18, 2002, the period for response being extended to May 18, 2003, by the Petition for Extension of Time filed herewith, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claims 11 and 12 as set forth below in clean form. Additionally, in accordance with 37 CFR 1.121(c)(1)(ii), amended claim(s) is/are set forth in a marked-up version in the page(s) attached to this Amendment.

11. (Amended) A semiconductor chip adapted for electrical connection to an external terminal, comprising:

a semiconductor chip body having a surface with internal wiring disposed thereon, at least one surface area of the internal wiring defining an external connection pad and at least one other surface area different from the at least one surface area of the internal wiring defining an internal connection pad, both the external connection pad and the internal connection pad facing in a same direction as the surface of the semiconductor chip body;

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1